Novelda AS
Analog IC Design

Jobbnorge ID: 262534
Deadline: 6/30/2024
Website: https://novelda.com/
Scope: Fulltime
Duration: Permanent

NOVELDA AS

Novelda provides the world's most accurate, low-power, and reliable sensor solution for human presence detection. Our UWB (Ultra-Wideband) impulse radar, no bigger than the tip of your finger, enables new levels of user experience, security and, significant energy savings for smart devices and buildings.

Our sensors are being used by global tech companies. As an example, we are integrated in several of Lenovo's flagship ThinkPad laptops. We are now expanding to new products within consumer electronics, smart home, building automation and automotive.

Our world-leading technology team includes SoC (System on Chip) IC design, signal processing, and machine learning experts.

See video of our sensor products

Novelda X4 (Proximity Sensor): https://vimeo.com/716749646
Novelda X4 (Occupancy Sensor): https://vimeo.com/723745642
Novelda X7 (Next Generation): https://vimeo.com/675449194

Do you want to join the Ultra-Wideband revolution?

Jr. Analog/Mixed-Signal IC Design Engineer

About the position
Is your favourite atomic number 14? Do you believe that good analog design is more of an art than science? Our mission is to make the world's best UWB radar SoCs for human presence detection, and we're looking for a curious and enthusiastic individual to strengthen our Analog/RF Design Team. In this role, you will actively work within the IC team to design and deliver IPs for Novelda's next-generation radar SoCs. We have an opportunity for a seasoned analog/mixed-signal engineer with background in high-speed/high-frequency (<10GHz) clocking circuitry.

Responsibilities
- Skilful in specifications, design, simulation, optimization, and layout of analog blocks such as bandgap references, biasing circuits, amplifiers, linear and switching (LDO) regulators, PLL/DLLs, switched-cap circuits, oscillators, and data converters.
- Post-layout and top-level simulations to validate top-level integration.
- Hands-on experience in lab bring-up, post-silicon validation, characterization, and troubleshooting.
- Interface with the Test Engineering, Product and Applications to successfully bring new products from initial concept through production release and volume ramp.

Qualifications
- The ideal candidate should have a thorough understanding of analog/mixed-signal design with 2+ years of relevant design experience.
- BS/MS in Electrical Engineering or equivalent. Relevant experience may compensate for lack of formal qualifications.
- Proficiency in transistor-level design, layout, and verification.
- Experience with ASIC design flows for deep submicron technologies, including state-of-the-art EDA tools for custom IC like Cadence Virtuoso, MMSIM/Spectre.
- An understanding of the challenges with DFT and DFM in CMOS.
- Proficiency with lab equipment and measurement techniques for silicon validation.
- Strong oral and written communication skills.

Other Skills
- Experience with system and block level behavioural modelling of analog/mixed-signal circuits in Verilog-A/AMS and MATLAB.
- Proficiency in programming and scripting languages like Python, SKILL, C++.

Exceptional candidates will be considered at all levels of experience.

Why start working at Novelda?

- Exciting challenges and team
- Unique and groundbreaking technology
- Exciting and demanding challenges in everyday work
- Opportunity to work with world-leading expertise
- Large degree of freedom and influence on company products
- Dynamic team and great working environment

We offer

- Competitive salary
- Fully paid maternity / paternity leave
- Flexible working hours
- Free phone and data package
- Solid pension scheme and employee insurances
- Subsidised canteen

Application requirements

Applicants must submit the following:

- Cover letter
- CV
- Transcripts

If you have questions, please contact Elizabeth Wisland at +47 95 84 77 82 / elizabeth.wisland@novelda.com

Additional information

Place of service:

Gjerdrums vei 8 0484 Oslo (Oslo Municipality)